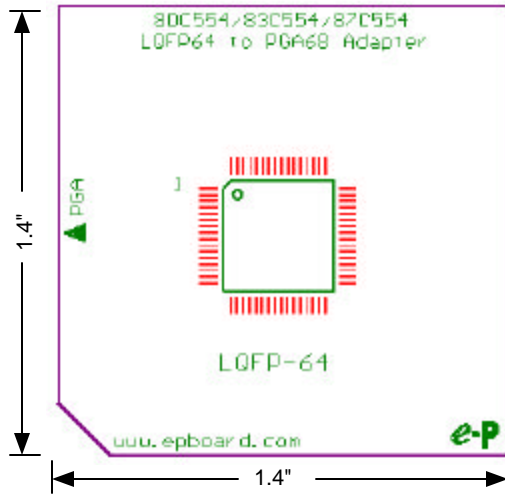
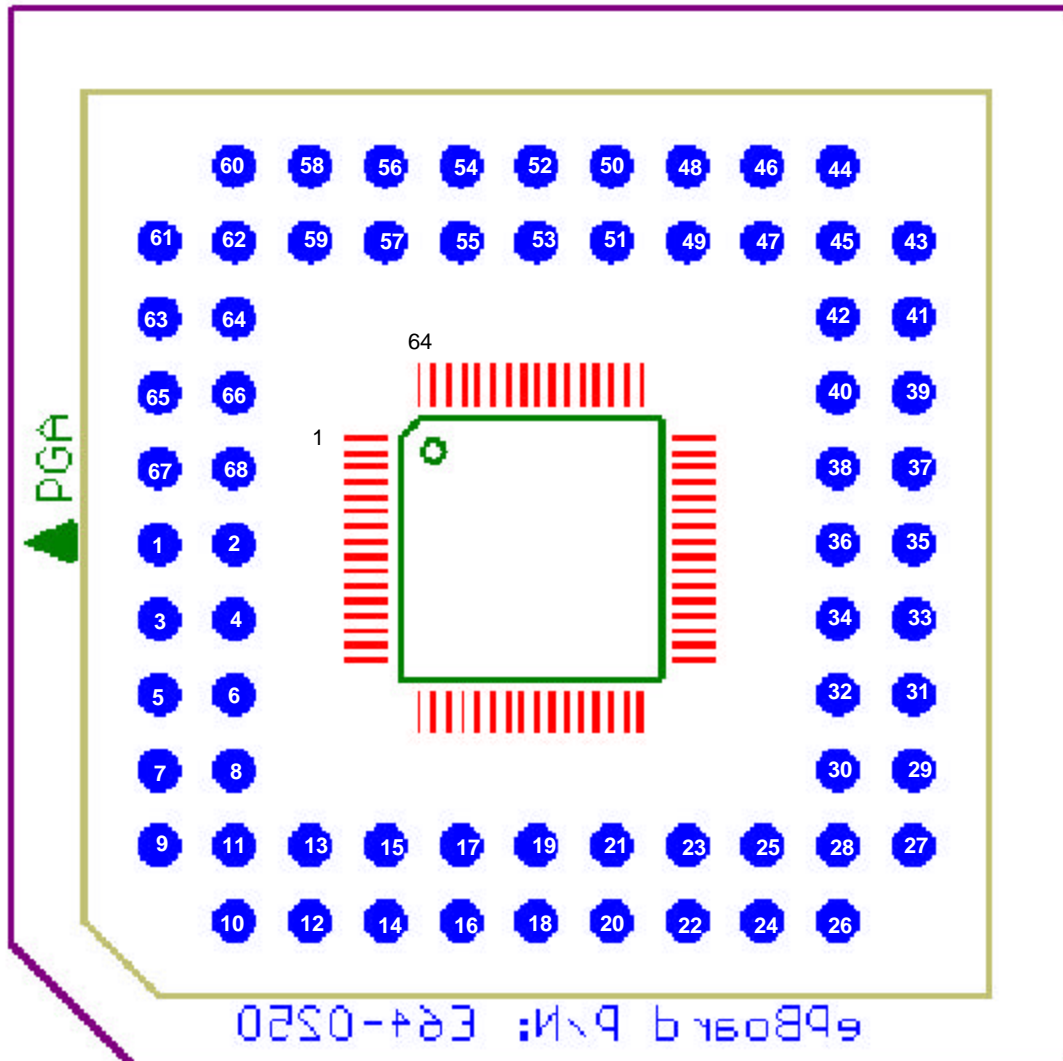
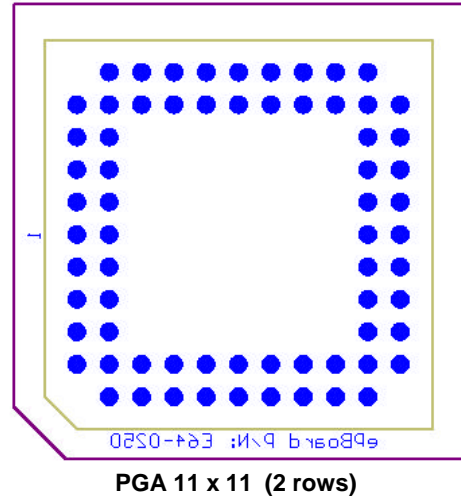


Top Side



Bottom Side (Top View X-Ray)



**Top View X-Ray
with PGA pin assignment**

LQFP64

Pin Function

1. AVDD
2. P5_6/ADC6
3. P5_5/ADC5
4. P5_4/ADC4
5. P5_3/ADC3
6. P5_2/ADC2
7. P5_1/ADC1
8. P5_0/ADC0
9. VDD
10. STADC
11. PWM0
12. PWM1
13. EW
14. P4_0/CMSR
15. P4_1/CMSR
16. P4_2/CMSR
17. P4_3/CMSR
18. P4_4/CMSR
19. P4_5/CMSR
20. P4_6/CMT0
21. P4_7/CMT1
22. RST
23. P1_0/CT0I
24. P1_1/CT1I
25. P1_2/CT2I
26. P1_3/CT3I
27. P1_4/RT2
28. P1_5/RT1
29. P1_6/SCL
30. P1_7/SDA
31. P3_0/RxD
32. P3_1/TxD
33. P2_2/INT0
34. P3_3/INT1
35. P3_4/T0
36. P3_5/T1
37. P3_6/WR
38. P3_7/RD
39. XTAL2
40. XTAL1
41. VSS
42. VSS
43. P2_0/A08
44. P2_1/A09
45. P2_2/A10
46. P2_3/A11
47. P2_4/A12
48. P2_5/A13
49. P2_6/A14
50. P2_7/A15
51. PSEN
52. ALE/PROG
53. EA/VPP
54. P0_7/AD7
55. P0_6/AD6
56. P0_5/AD5
57. P0_4/AD4
58. P0_3/AD3
59. P0_2/AD2
60. P0_1/AD1
61. P0_0/AD0
62. Avref-
63. Avref+
64. AVSS

- PGA 68
- Pin Function
1. P5_0/ADC0
 2. VDD
 3. STADC
 4. PWM0
 5. PWM1
 6. EW
 7. P4_0/CMSR0
 8. P4_1/CMSR1
 9. P4_2/CMSR2
 10. P4_3/CMSR
 11. P4_4/CMSR
 12. P4_5/CMSR
 13. P4_6/CMT0
 14. P4_7/CMT1
 15. RST
 16. P1_0/CT0I
 17. P1_1/CT1I
 18. P1_2/CT2I
 19. P1_3/CT3I
 20. P1_4/RT2
 21. P1_5/RT1
 22. P1_6/SCL
 23. P1_7/SDA
 24. P3_0/RxD
 25. P3_1/TxD
 26. P3_2/INT0
 27. P3_3/INT1
 28. P3_4/T0
 29. P3_5/T1
 30. P3_6/WR
 31. P3_7/RD
 32. NC
 33. NC
 34. XTAL2
 35. XTAL1
 36. VSS
 37. VSS
 38. NC
 39. P2_0/A08
 40. P2_1/A09
 41. P2_2/A10
 42. P2_3/A11
 43. P2_4/A12
 44. P2_5/A13
 45. P2_6/A14
 46. P2_7/A15
 47. PSEN
 48. ALE/PROG
 49. EA/V PP
 50. P0_7/AD7
 51. P0_6/AD6
 52. P0_5/AD5
 53. P0_4/AD4
 54. P0_3/AD3
 55. P0_2/AD2
 56. P0_1/AD1
 57. P0_0/AD0
 58. Avref-
 59. Avref+
 60. AVSS
 61. AVDD
 62. P5_7/ADC7
 63. P5_6/ADC6
 64. P5_5/ADC5
 65. P5_4/ADC4
 66. P5_3/ADC3
 67. P5_2/ADC2
 68. P5_1/ADC1

U2		U1	
P50/ADC0	1	P51/ADC1	68
VDD	2	P52/ADC2	67
STADC	3	P53/ADC3	66
PWM0	4	P54/ADC4	65
PWM1	5	P55/ADC5	64
EW	6	P56/ADC6	63
P40/CMSR0	7	P57/ADC7	62
P41/CMSR1	8	AVDD	61
P42/CMSR2	9	AVSS	60
P43/CMSR3	10	AVREF+	59
P44/CMSR4	11	AVREF-	58
P45/CMSR5	12	P00/AD0	57
P46/CMT0	13	P01/AD1	56
P47/CMT1	14	P02/AD2	55
RST	15	P03/AD3	54
P10/CT0I	16	P04/AD4	53
P11/CT1I	17	P05/AD5	52
P12/CT2I	18	P06/AD6	51
P13/CT3I	19	P07/AD7	50
P14/RT2	20	EA/VPP	49
P15/RT1	21	ALE/PROG	48
P16/SCL	22	PSEN	47
P17/SDA	23	P27/AT5	46
P30/RxD	24	P28/AT4	45
P31/TxD	25	P29/AT3	44
P32/INT0	26	P24/AT2	43
P33/INT1	27	P23/AT1	42
P34/T0	28	P22/A10	41
P35/T1	29	P21/A09	40
P36/WR	30	P20/A08	39
P37/RD	31	VSS	38
XTAL2	32	VSS	37
XTAL1	33	XTAL2	36
	34	XTAL1	35

PGA 68 11 x 11

U1		U2	
AVDD	1	P50/ADC0	68
P56/ADC6	2	VDD	67
P55/ADC5	3	STADC	66
P54/ADC4	4	PWM0	65
P53/ADC3	5	PWM1	64
P52/ADC2	6	EW	63
P51/ADC1	7	P40/CMSR0	62
P50/ADC0	8	P41/CMSR1	61
VDD	9	P42/CMSR2	60
STADC	10	P43/CMSR3	59
PWM0	11	P44/CMSR4	58
PWM1	12	P45/CMSR5	57
EW	13	P46/CMT0	56
P40/CMSR0	14	P47/CMT1	55
P41/CMSR1	15	RST	54
P42/CMSR2	16	P10/CT0I	53
P43/CMSR3	17	P11/CT1I	52
P44/CMSR4	18	P12/CT2I	51
P45/CMSR5	19	P13/CT3I	50
P46/CMT0	20	P14/RT2	49
P47/CMT1	21	P15/RT1	48
RST	22	P16/SCL	47
P10/CT0I	23	P17/SDA	46
P11/CT1I	24	P30/RxD	45
P12/CT2I	25	P31/TxD	44
P13/CT3I	26	P32/INT0	43
P14/RT2	27	P33/INT1	42
P15/RT1	28	P34/T0	41
P16/SCL	29	P35/T1	40
P17/SDA	30	P36/WR	39
P30/RxD	31	P37/RD	38
P31/TxD	32	XTAL2	37
	33	XTAL1	36
	34	VSS	35
	35	VSS	34

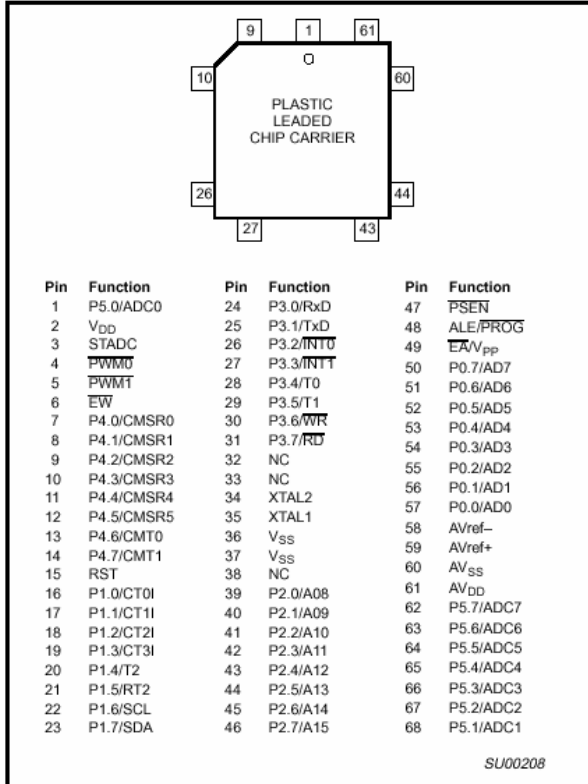
LQFP64
80C554/83C554/87C554

eP Board Design		
Title		
80C554 LQFP64 to PGA68 TOP PART		
Size	Document Number	Rev
B	E64-0250T	1.0
Date: Wednesday, December 11, 2002		Sheet 1 of 1

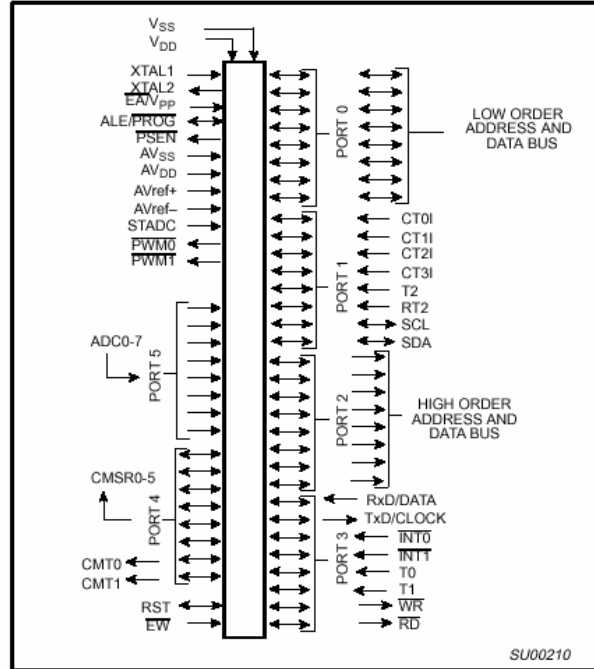
Philips 80C51 8-bit microcontroller: **P87C554, P87C552**

PLCC 68 PIN CONFIGURATIONS:

Plastic Leaded Chip Carrier pin functions



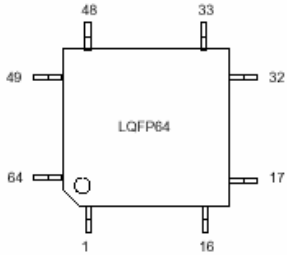
LOGIC SYMBOL



Philips 80C51 8-bit microcontroller: **80C554/83C554/87C554**

LQFP 64 PIN CONFIGURATIONS:

Plastic Quad Flat Pack pin functions



Pin	Function	Pin	Function	Pin	Function	Pin	Function
1.	AV _{DD}	17.	P4.3/CMSR3	33.	P3.2/INT0	49.	P2.8/A14
2.	P5.6/ADC6	18.	P4.4/CMSR4	34.	P3.3/INT1	50.	P2.7/A15
3.	P5.5/ADC5	19.	P4.5/CMSR5	35.	P3.4/T0	51.	PSEN
4.	P5.4/ADC4	20.	P4.6/CMT0	36.	P3.5/T1	52.	ALE/PROG
5.	P5.3/ADC3	21.	P4.7/CMT1	37.	P3.6/WR	53.	EA/V _{PP}
6.	P5.2/ADC2	22.	RST	38.	P3.7/RD	54.	P0.7/AD7
7.	P5.1/ADC1	23.	P1.0/CT0I	39.	XTAL2	55.	P0.6/AD6
8.	P5.0/ADC0	24.	P1.1/CT1I	40.	XTAL1	56.	P0.5/AD5
9.	V _{DD}	25.	P1.2/CT2I	41.	V _{SS}	57.	P0.4/AD4
10.	STADC	26.	P1.3/CT3I	42.	V _{SS}	58.	P0.3/AD3
11.	PWM0	27.	P1.4/T2	43.	P2.0/A08	59.	P0.2/AD2
12.	PWM1	28.	P1.5/RT2	44.	P2.1/A09	60.	P0.1/AD1
13.	EW	29.	P1.6/SCL	45.	P2.2/A10	61.	P0.0/AD0
14.	P4.0/CMSR0	30.	P1.7/SDA	46.	P2.3/A11	62.	AV _{ref-}
15.	P4.1/CMSR1	31.	P3.0/RxD	47.	P2.4/A12	63.	AV _{ref+}
16.	P4.2/CMSR2	32.	P3.1/TxD	48.	P2.5/A13	64.	AV _{SS}

SU01444

LOGIC SYMBOL

